A Task Duplication Based Scheduling Algorithm for Avoiding Useless Duplication

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Abstract: In this paper, we propose a task duplication based scheduling algorithm for avoiding useless duplication. In our algorithm, task duplication is divided into two phases: a task fill phase and a task duplication phase. In the task fill phase, tasks are duplicated and allocated to idle time slots in used processors. Namely, the task fill phase does not require additional idle processors. The task duplication phase is invoked only if there are idle processors to be left for a new allocation. Thus, our algorithm can consume computing facilities effectively. Experimental results show that our algorithm can achieve less computation time and less use of idle processors.

Keywords: scheduling algorithm, task duplication, cluster computing, parallelizing compiler

1 Introduction

In parallel processing, a task scheduling algorithm is one of the most important topics for the sake of effective use of computing facilities. Especially, as the number of processors in a system increases, management of idle processors is very essential. Task duplication based (TDB) scheduling algorithms[1, 2] are the ideas for decreasing the number of idle processors. In task duplication based algorithms, a task is duplicated and allocated to plural processors. Namely, the same task is executed in plural processors simultaneously. By simultaneous execution of the same task, other tasks which have dependency relationships with the duplicated task can be executed earlier, and thus the processing time of whole tasks can be decreased.

Many task duplication based scheduling algorithms have been proposed[3, 4, 5]. In these algorithms, all tasks are examined whether it can be duplicated or not, namely this duplication induces decrease of computation time of the whole tasks or not. In other words, it is assumed that there exist a lot of idle processors so that duplicable tasks can be allocated to new idle processors. Therefore, these algorithms have such disadvantages that too many tasks are duplicated and many idle processors have to be consumed. Furthermore, in limited number of processors environment, inadequate tasks are duplicated and the computation time of the whole tasks cannot be decreased.

In this paper, we propose a task duplication based scheduling algorithm for avoiding useless duplication. In our algorithm, tasks are duplicated if the duplication leads decrease of computation time. Namely, only effective duplication is achieved in our algorithm. So, our algorithm is suitable for the environment in which there are not enough idle processors.

The rest of this paper is organized as follows. In Section 2, a DAG and other notations
are introduced as a task scheduling model, and task duplication based scheduling algorithms are described in detail. In Section 3, our proposed algorithm is described. In Section 4, experimental results are explained in order to confirm the advantage of our algorithm. In Section 5, our conclusion and future work are described.

2 Task Duplication Based Algorithms

In this section, we describe current task duplication based scheduling algorithms, and the problems in these algorithms. Before the detailed description of algorithms, we explain a model of task scheduling. Namely, a DAG and other notations are described.

2.1 Task Scheduling Model

Generally, in task scheduling, an actual parallel application is transformed into a Directed Acyclic Graph (DAG). In DAG, a task is denoted as a node, and a dependency relationship between tasks is denoted as an edge. Each node has a computation cost, and each edge has a communication cost. Figure 1(a) shows a typical DAG. The following is the notation of our task scheduling model.

- DAG: \( G = (V, E) \).
- \( V \): set of task \( T_i \).
- \( E \): set of dependency \( (T_j, T_k) \).
- \( cost(T_i) \) [ut]: computation cost of a task \( T_i \).
- \( comm(T_i, T_j) \) [ut]: communication cost between two tasks \( T_i \) and \( T_j \).
- \( P \): set of available processor \( p_i \).

The [ut] stands for a unit time.

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Figure 1: DAG and task duplication based scheduling

In our task scheduling model, if two depended tasks are allocated to the same processor, the communication cost between two tasks is regarded as zero. Figure 1(b) shows an example task scheduling. Since \( T_1 \) and \( T_2 \) are allocated to the same processor \( p_0 \), the communication cost between two tasks becomes zero and \( T_2 \) is allocated just after the completion of execution \( T_1 \).

2.2 Duplication Based Algorithms

As shown in Figure 1(b), the execution time for the whole tasks is 10[ut] without task duplication. This is because communication between \( T_0 \) and \( T_2 \) occurs and the start time of \( T_2 \) is 6[ut]. When \( T_0 \) is duplicated and allocated to both \( p_0 \) and \( p_1 \), the communication cost can be ignored. Figure 1(c) shows this situation. No communication overhead occurs and thus the whole execution time becomes 7[ut]. This is a basic concept of task duplication based scheduling algorithms.

Several task duplication based scheduling algorithms have been proposed. First, in the algorithm, all the tasks are grouped into some task clusters, and each task cluster is allocated to a processor. Then, if there exists idle processors, tasks are duplicated and allocated to the idle processors in order to minimize the
Figure 2: Task duplication based scheduling

total computing time of tasks.

The processing steps of the algorithm is described with an example DAG shown in Figure 2(a). First, a task cluster is generated. For a DAG in Figure 2(a), task clusters are organized as follows: \{T_1, T_3, T_6, T_8, T_{11}\}, \{T_1, T_2, T_7, T_9\} and \{T_1, T_3, T_5, T_{10}\}. Task clusters are allocated to processors individually. The result of allocation for task clusters is shown in Figure 2(b). In generation of task clusters, the number of available processors are not taken into account. Thus, when the size of DAG is large, the number of generated task clusters also increases. This is one of the disadvantage in algorithms.

After allocating task clusters, task duplication is achieved. In the task duplication phase, tasks which are allocated to the tail of each processors are focused on. For these tasks, if the predecessor task does not has the highest value of level, this task cluster is duplicated and tasks in another task cluster are assigned to the front of the task. Here, level is defined as the computation cost to the exit node. For example, we focus on T_9 in Figure 2(b). T_7 is the predecessor task of T_9. As shown in Figure 2(a), T_9 depends on T_5, T_6, T_7, and T_6 has the highest value of level (T_6 \rightarrow T_8 \rightarrow T_{11}). So, the task cluster is duplicated and the other task cluster which includes T_6 is assigned. This operation is repeated until all the predecessors of the task have the highest value of level. Figure 3 shows the processing result of the task duplication phase. This phase also requires that the enough number of idle processors are used for task duplication.

3 Our Proposed Algorithm

3.1 Concept of Our Algorithm

As shown in the previous section, current task duplication based scheduling algorithms requires the enough number of idle processors for generating task clusters and task duplication. As the size of application programs becomes large, the number of tasks in the application programs also increases and thus the number of task clusters increases rapidly. Therefore, effectiveness of task duplication is not so improved in actual systems in which limited number of processors can be used.

In order to solve the above problem, we pro-
pose a task duplication based scheduling algorithm which avoids useless duplication and uses limited number of idle processors effectively. Our algorithm is based on traditional BNP (Bounded Number of Processors) scheduling algorithms[6, 7]. Our algorithm consists of three phases.

1. BNP scheduling phase: tasks are allocated to processors by a traditional BNP algorithm.

2. Task full phase: if idle time slots exist in processors, a task is duplicated and allocated to the time slot.

3. Task duplication phase: the task which has the highest effectiveness of task duplication is duplicated to an idle processor.

In the first and second phases, extra idle processors are not consumed. Namely, in our method, task allocation is achieved in minimum number of processors in the first and second phases. The third phase is invoked only if there exist idle processors in a system. Thus, on small systems which consists of small number of processors, effective task duplication based allocation is achieved in the first and second phases. Additionally, on large systems in which idle processors are left unused, the third phase is invoked and tasks are duplicated and allocated to remaining idle processors. Therefore, our algorithm can be adapted effectively to both small and large systems.

### 3.2 Task Fill

In the task fill phase, a task is duplicated and allocated to idle time slots in a processor, which makes the execution time of the whole tasks short without using additional idle processors.

The example of task fill is shown in Figure 4. The top figure in Figure 4(b) is a result of task allocation for the DAG shown in Figure 4(a) by a BNP scheduling algorithm. In this result, the execution of $T_2$ and $T_3$ is started when the execution of $T_0$ is finished and communication among them is finished. In the task fill phase, $T_0$ is duplicated and allocated to the idle time slots $p_1$ and $p_2$, which reduces the waiting time of $T_2$ and $T_3$. Additionally, there is an idle time slot between $T_2$ and $T_5$, and $T_3$ can be also duplicated to this time slot. By this processing, as shown in Figure 4(b), communication between tasks can be deleted and the start time of tasks can be accelerated.

Figure 5 shows notations used in the algorithm and Figure 6 shows the task fill algorithm. For each task, the task fill condition is evaluated: there are idle time slots enough to be allocated to the task which is depended upon by the focused task, in front of the focused task. If there are enough idle time slots, the depended task is duplicated and allocated in front of the focused task. By task fill, the communication time can be deleted between two tasks and the computation time of the whole tasks can be reduced even though the number of used processors is fixed.
3.3 Task Duplication

In the task duplication phase, it is important to select tasks to be duplicated so that the duplication makes the computation time of the whole tasks short. In order to avoid useless duplications, we adopt a depth first search method in selecting tasks to be duplicated. First, a candidate task for duplication is selected. In this selection, a candidate task is searched from the tail task to the head task. This is because a duplication of head tasks is more effective than that of tail tasks. So, in our algorithm, a head task which satisfies two conditions is selected as a candidate task. These conditions are as follows.

1. The task in front of a candidate task is in a best predecessor group of a candidate task.
2. There are no idle time slots among tasks in a best predecessor group of a candidate task.

Figure 5: Notation

Figure 6: Algorithm in task fill phase

Figure 7: Algorithm in task duplication phase

4 Performance Evaluation

In order to verify the effectiveness of our algorithm, we perform some simulated experiments. In experiments, the Standard Task Graph Set (STG) is adopted as example DAGs[8]. The STG is a kind of a benchmark for evaluation of multiprocessor scheduling algorithms. The STG consists of task graphs generated randomly and modeled from actual application programs. However, communication cost between tasks is not included in STG. Thus, we assign communication costs to communication links randomly. In the first experiment, we compare the computation times of the whole tasks in both a tra-
Table 1: Comparison of BNP and our algorithm

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<th>Our Algo. (sec)</th>
<th>Speed Up</th>
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Table 2: Comparison of TDB and our algorithm

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<th>Speed Up</th>
<th>Num. of Proc. in our algo.</th>
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ditional BNP scheduling algorithm and our algorithm. This experiment shows the effectiveness of the task fill phase and the task duplication phase. In the second experiment, we compare the computation times of the whole tasks and the number of processors used in traditional TDB algorithm and our algorithm. This experiment shows whether useless duplication occurs or not.

Table 1 shows the experimental result for comparing the computation times by traditional BNP algorithm and our algorithm. This result shows that computation time of all DAGs by our algorithm is less than that of the traditional BNP algorithm. From this experimental result, we can conclude that task duplication strategy of our algorithm is correct.

Table 2 shows the computation times and the number of processors used in both the traditional TDB algorithm and our algorithm. This shows that for all DAGs, TDB algorithm consumes more additional processors although the computation time is not so shortened. From this experimental result, we clarify that useless task duplication can be prevented in our algorithm.

From these experimental results, we can
conclude that our task duplication based algorithm prevents useless task duplications and makes the whole computation time of DAGs short.

5 Conclusion

In this paper, we proposed a task duplication based scheduling algorithm. Our algorithm takes reduction of useless task duplication into account. In order to reduce the useless task duplication, the process of task duplication is divided into two phases in our algorithm: the task fill phase and the task duplication phase. In the task fill phase, tasks are duplicated and assigned to idle time slots. In this phase, additional idle processors are not required. In task duplication phase, tasks are duplicated and assigned to idle processors. In this phase, in order to prevent useless task duplication, a depth-first searching is adopted and only tasks required to be duplicated are duplicated. From experimental results, in comparison with other algorithms, our algorithm makes the whole computation time short and makes the number of used processors small.

For future work, we have to evaluate our algorithm with actual application programs. In addition, we have to evaluate our algorithm with an environment in which limited number of computing facilities can be used.

References


